

EW026157252

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/444,024
Priority Filing Date November 19, 1999
Inventor Jigish D. Trivedi et al.
Assignor Micron Technology, Inc.
Priority Group Art Unit 2811
Priority Examiner D. Owens
Attorney's Docket No. MI22-1965
Title: Integrated Circuitry

Handwritten: 4292

J1046 U.S. PTO
10/087416
02/27/02

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§1.56, 1.97 and §1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending Application Serial No. 09/444,024, filed November 19, 1999, upon which the above-identified application relies for a priority date under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 2-27-02



D. Brent Kenady
Reg. No. 36,138
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828
(509) 624-4276
Customer No. 021567